

detecting an edge of a pulse of each sampled component;

counting the clock cycles between edges;

comparing the counted clock cycles (EEC) with a prestored reference-value (EEC_0) in order to output a deviation value (RJ) as a measure for the instantaneous quality of each digital signal (S);

feeding the deviation value (RJ) to an absolute-value limiter unit (42) that provides an absolute deviation value (LPJ) and feeding the absolute deviation value (LPJ) to a storage latch (43) that outputs the absolute deviation value (PJ);

feeding the absolute deviation value (PJ) to a leaky integrator (50) that outputs a signal quality measure (J);

detecting from the at least two digital signals the signal with the best signal quality measure (PCS) and the second-best signal quality measure (DCS) and defining them as the first component (PCS) and the second component (DCS);

selecting the first component (PCS) and the second component (DCS); and

accessing a probability table (110) for providing a value (DDS) representative of the pulse position in response to

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receipt of at least one symbol of the first component (PCS) and at least one symbol of the second component (DCS).

19. (Amended) A receiver system (80) comprising:

Amended

an apparatus (101) for determining a pulse position for a signal encoded by pulse modulation, the signal comprising a first component (PCS) and a second component (DCS), the apparatus including a determination unit (118) comprising a probability table (110) for providing a value (DDS) representative of the pulse position in response to receipt of at least one symbol of the first component (PCS) and at least one symbol of the second component (DCS); and

a channel multiplexer (70) for determining the quality of at least two components (S), the channel multiplexer (70) comprising

a sampler (10) using clock cycles (CLK) for sampling of one of the at least two digital signals (S) with a number n of samples per defined pulse width, whereby $n \geq 1$;

an edge detector (20) for detecting an edge of a pulse of each sampled digital signal;

a counter (30) for counting the clock cycles between edges detected by the edge detector;

a deviation detector (40) being able to compare the counted clock cycles (EEC) with a prestored reference-value (EEC₀) in order to provide a deviation value (RJ) as a measure for the instantaneous quality of the digital signal (S);

a minimum-maximum detector (72) for detecting from the at least two digital signals the signal with the best signal quality measure (PCS) and the second-best signal quality measure (DCS) and defining them as the first component (PCS) and the second component (DCS); and

a diversity multiplexer (74) for selecting the first component (PCS) and the second component (DCS), these components (PCS, DCS) being feedable as input to the apparatus (101).

Please add new claims 22 and 23 as follows:

22. (New) A computer program comprising program code means for performing, when said program is run on a computer, a method for determining a pulse position for a signal encoded by pulse modulation, the signal comprising a first component (PCS) and a second component (DCS), the method comprising the step of:

providing, via a probability table (110), a value (DDS) representative of the pulse position in response to receipt of at least one symbol of the first component (PCS) and at least one symbol of the second component (DCS).